



Application Serial No. 10/626,228 - Filed July 24, 2003

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<p>Form PTO-1449 (2 modified)</p> <p>List of Patents and Publications</p> <p>For Applicant's Information</p> <p>Disclosure Statement</p> <p>(Use several sheets if necessary)</p>	<p>ATTY. DKT. NO. 5681-03800</p> <p>APPLICANT: Smith, et al.</p> <p>FILING DATE: July 24, 2003</p>	<p>SERIAL NO. 10/626,228</p> <p>GROUP: <u>Unknown</u> 2611</p>
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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

YT	A6	"XILINX: Using the Virtex Delay-Locked Loop"; XAPP132 October 21, 1998 (Version 1.31); Advanced Application Note; pgs. 1-14.
YT	A7	U.S. Patent Application Serial No. 10/117,427 filed April 5, 2002.

EXAMINER: /Young Tse/

DATE CONSIDERED: 10/01/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.